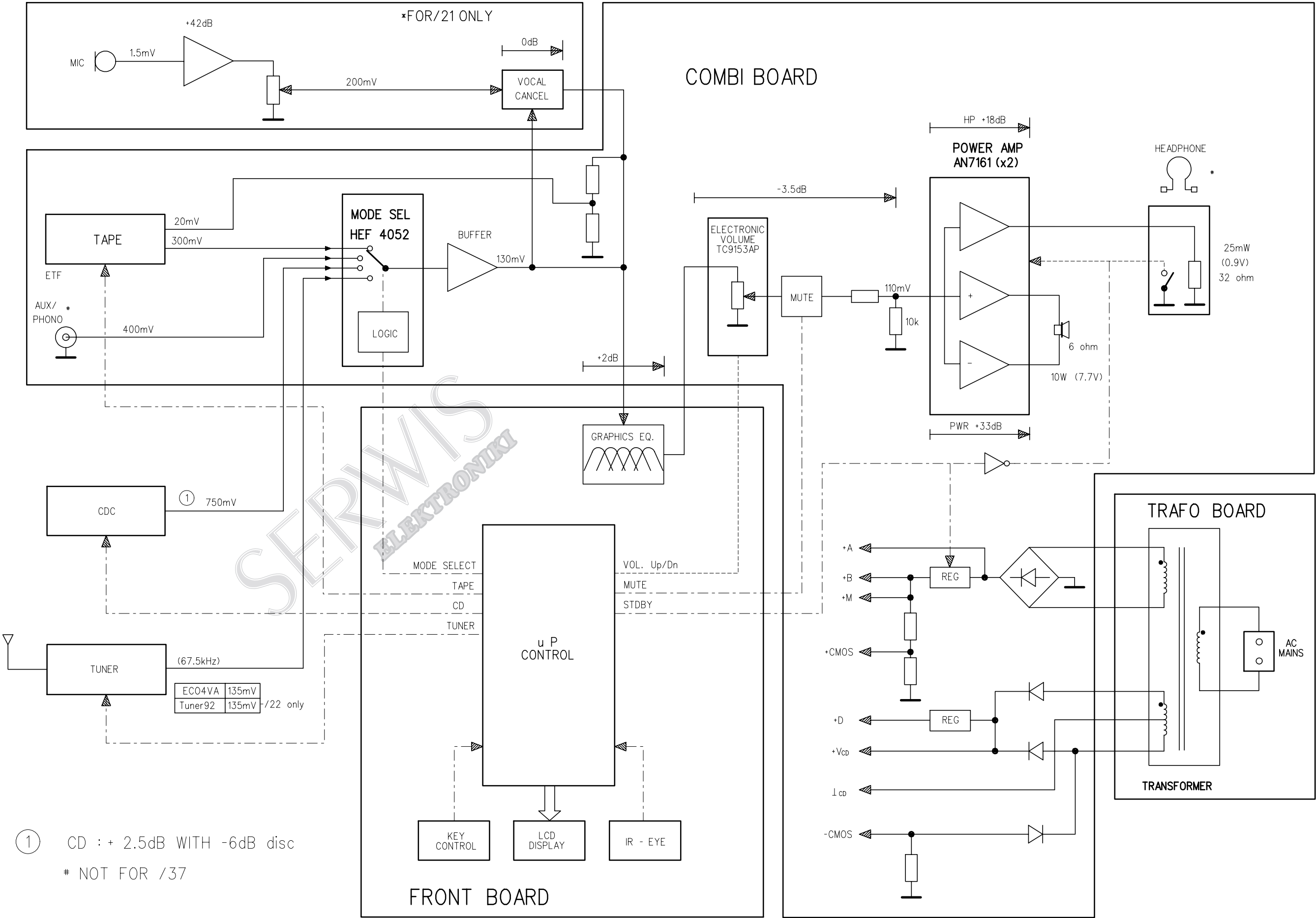
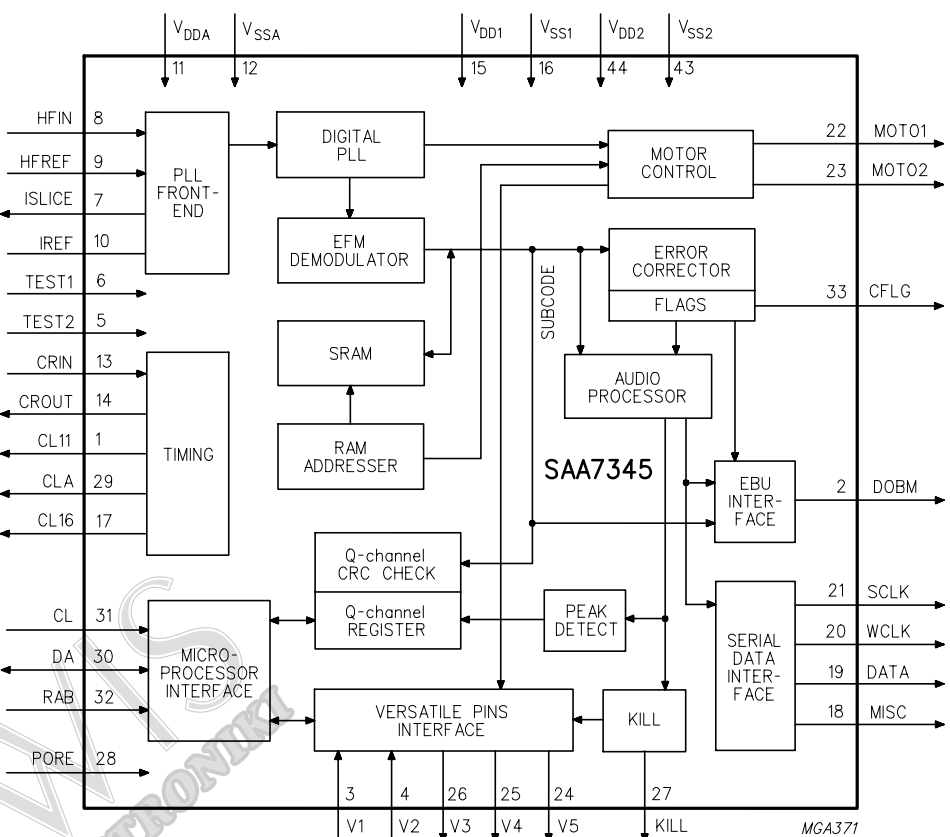


BLOCK DIAGRAM



IC 7860 (SAA 7345)

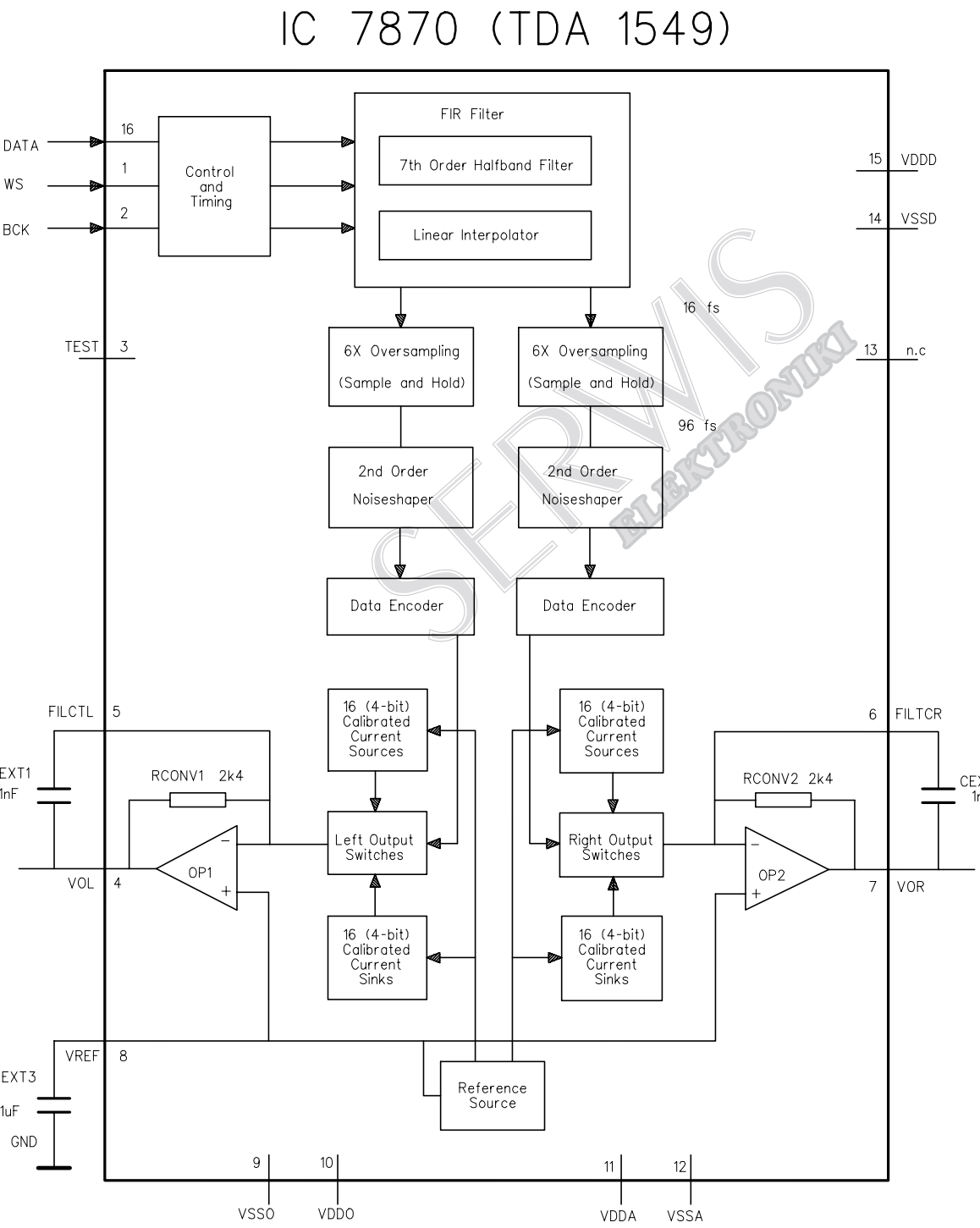
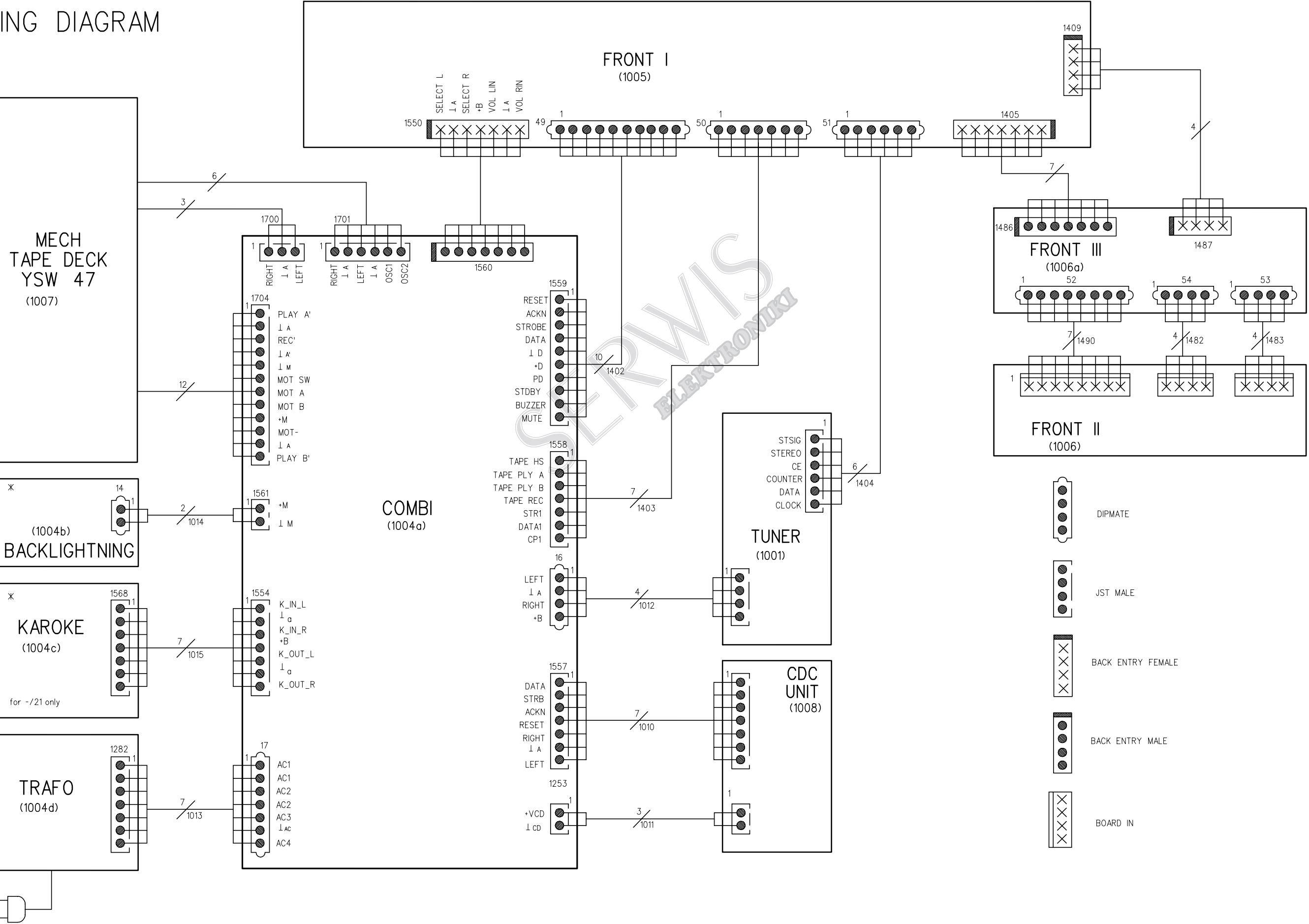


PINNING OF SAA7345

SYMBOL	PIN	DESCRIPTION	SYMBOL	PIN	DESCRIPTION
CL11	1	11.2896 MHz clock output (3-state)	DATA	19	serial data output (3-state)
DOBM	2	bi-phase mark output (externally buffered; 3-state)	WCLK	20	word clock output (3-state)
V1	3	versatile input pin	SCLK	21	serial bit clock (3-state)
V2	4	versatile input pin	MOTO1	22	motor output 1; versatile (3-state)
TEST2	5	test input; this pin should be tied LOW	MOTO2	23	motor output 2; versatile (3-state)
TEST1	6	test input; this pin should be tied LOW	V5	24	versatile output pin
ISLICE	7	current feedback from data slicer	V4	25	versatile output pin
HFIN	8	comparator signal input	V3	26	versatile output pin (open-drain)
HREF	9	comparator common-mode input	KILL	27	kill output; programmable (open-drain)
IREF	10	reference current pin (nominally V _{DD} /2)	PORE	28	power-on reset enable input (active LOW)
V _{DDA}	11	analog supply	CLA	29	4.2336MHz microprocessor clock output
V _{SSA}	12	analog supply	RAB	32	interface R/W and acknowledge input
CRIN	13	crystal/resonator input	CFLG	33	correction flag output (open-drain)
CROUT	14	crystal/resonator output		34-42	no internal connection
V _{DD1}	15	digital supply	V _{SS2}	43	digital supply
V _{SS1}	16	digital supply	V _{DD2}	44	digital supply
CL16	17	16.9344MHz system clock output (3-state)			
MISC	18	general purpose DAC output (3-state)			

DW_SE1/2003

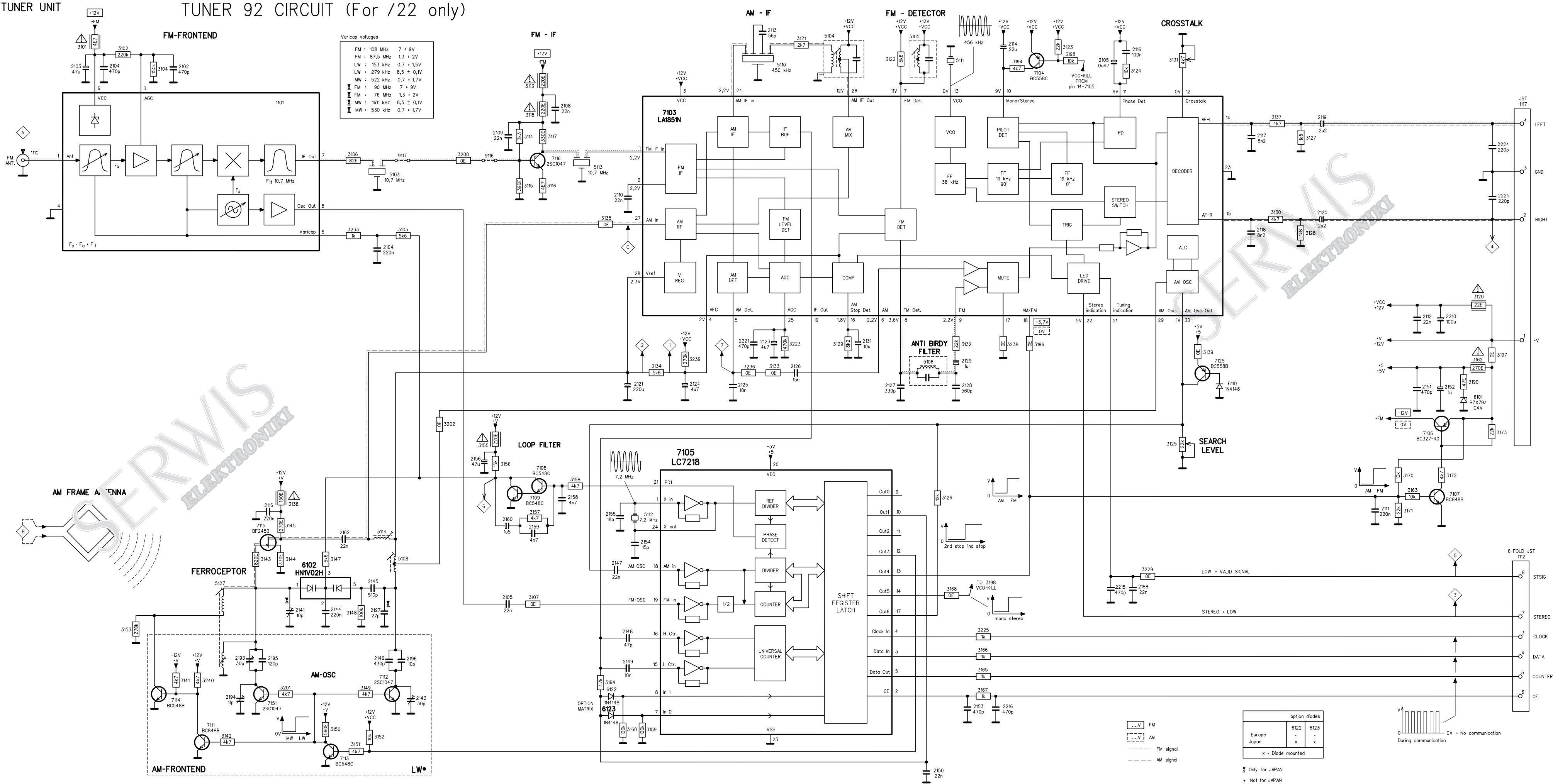
WIRING DIAGRAM



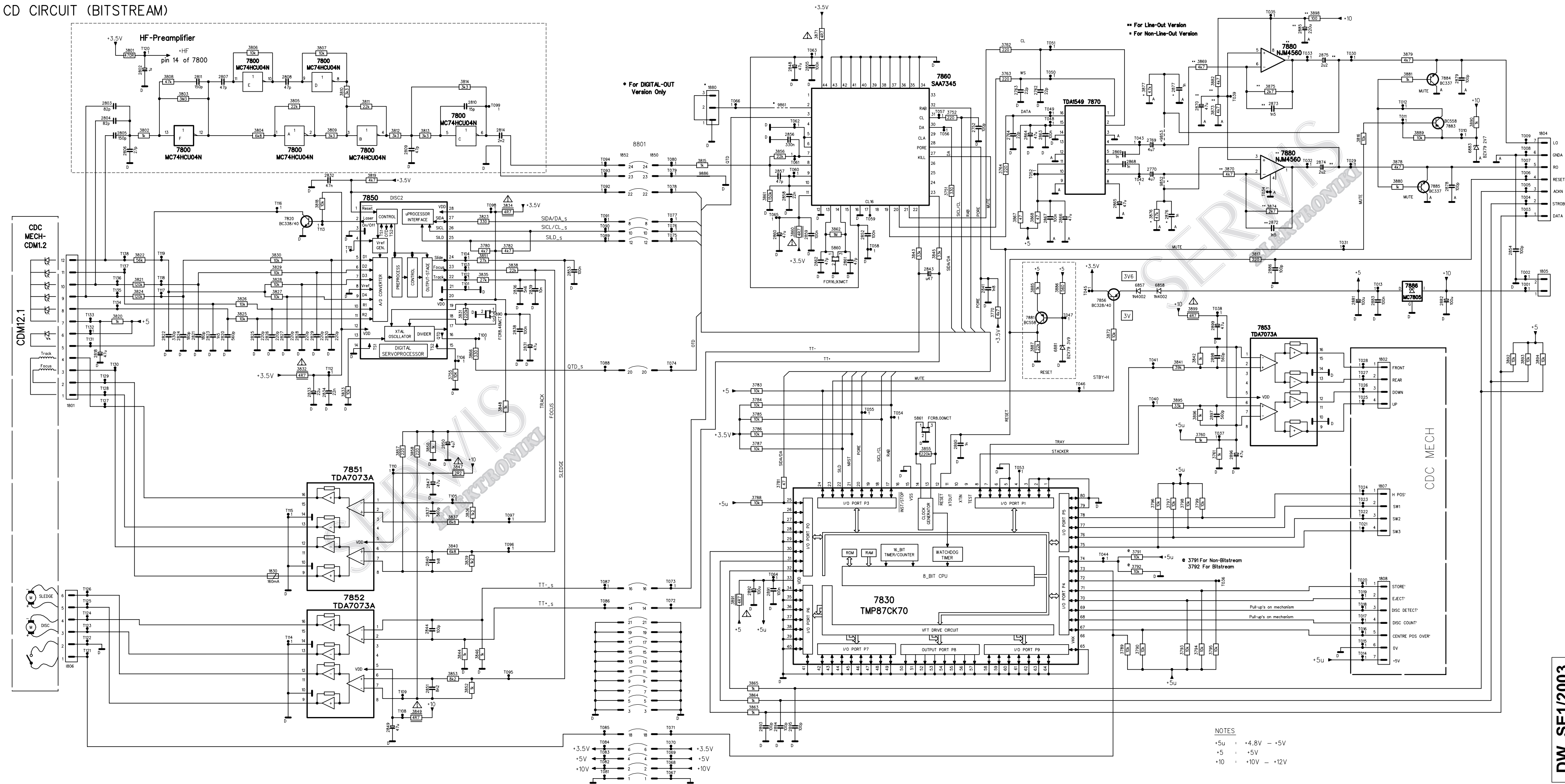
DW_SE1/2003

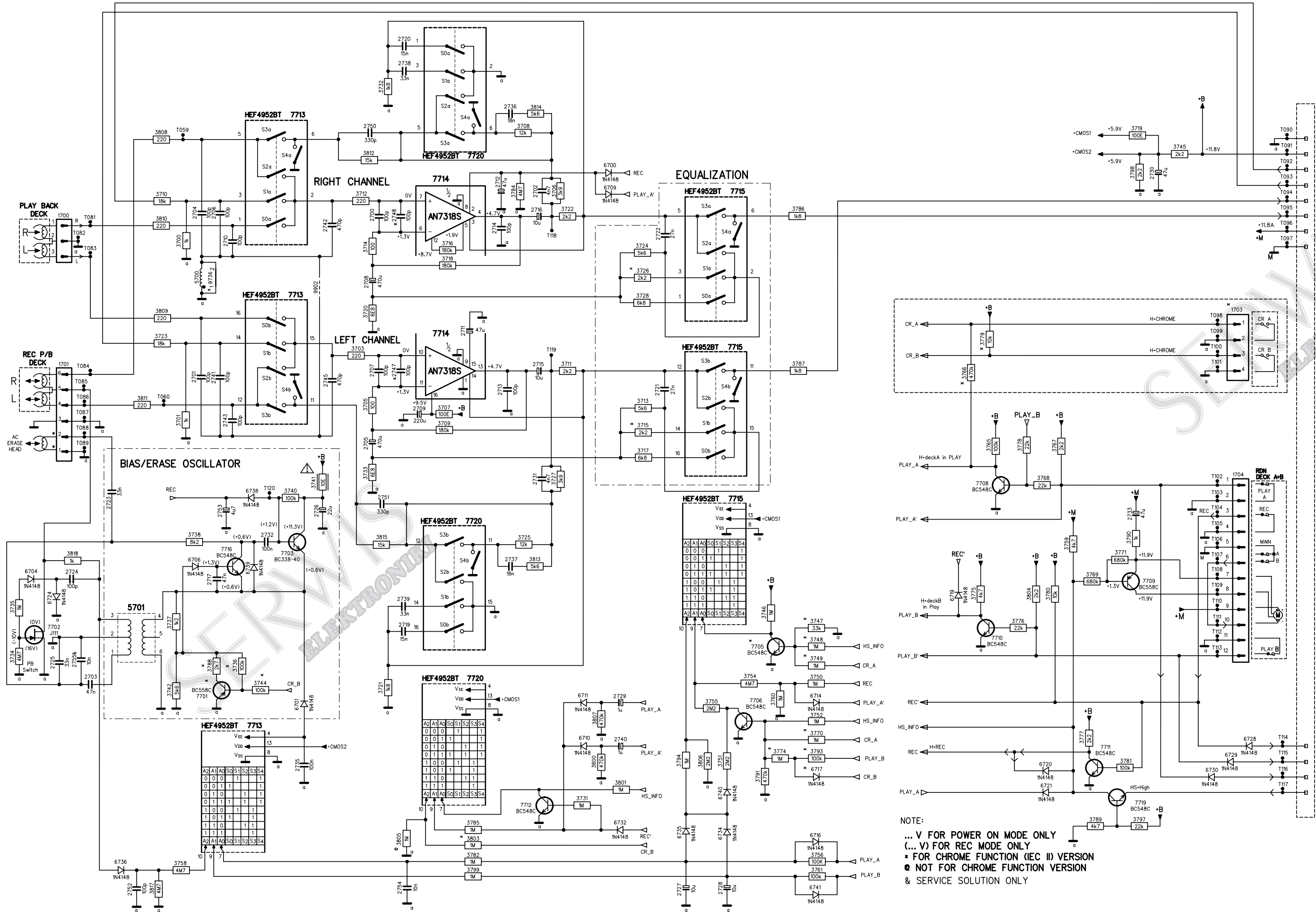
Zestaw audio Philips FW36/20/21/22/25/30/37
(strona 2/12)



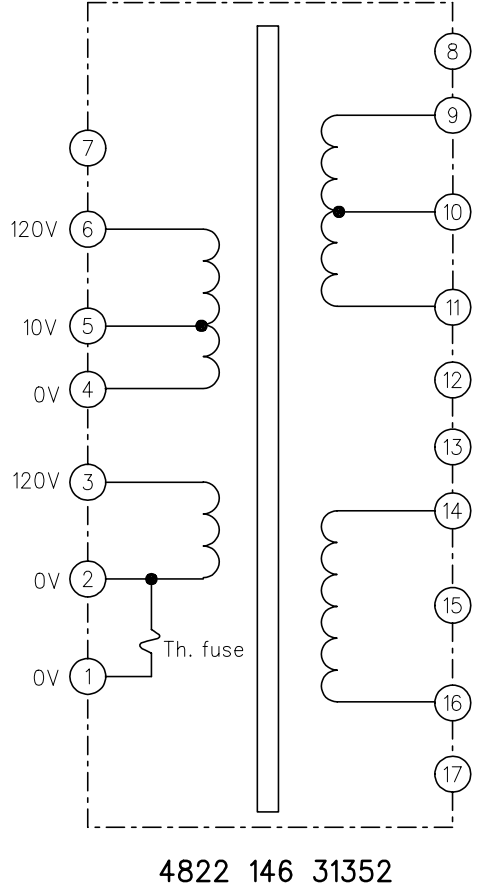


CD CIRCUIT (BITSTREAM)



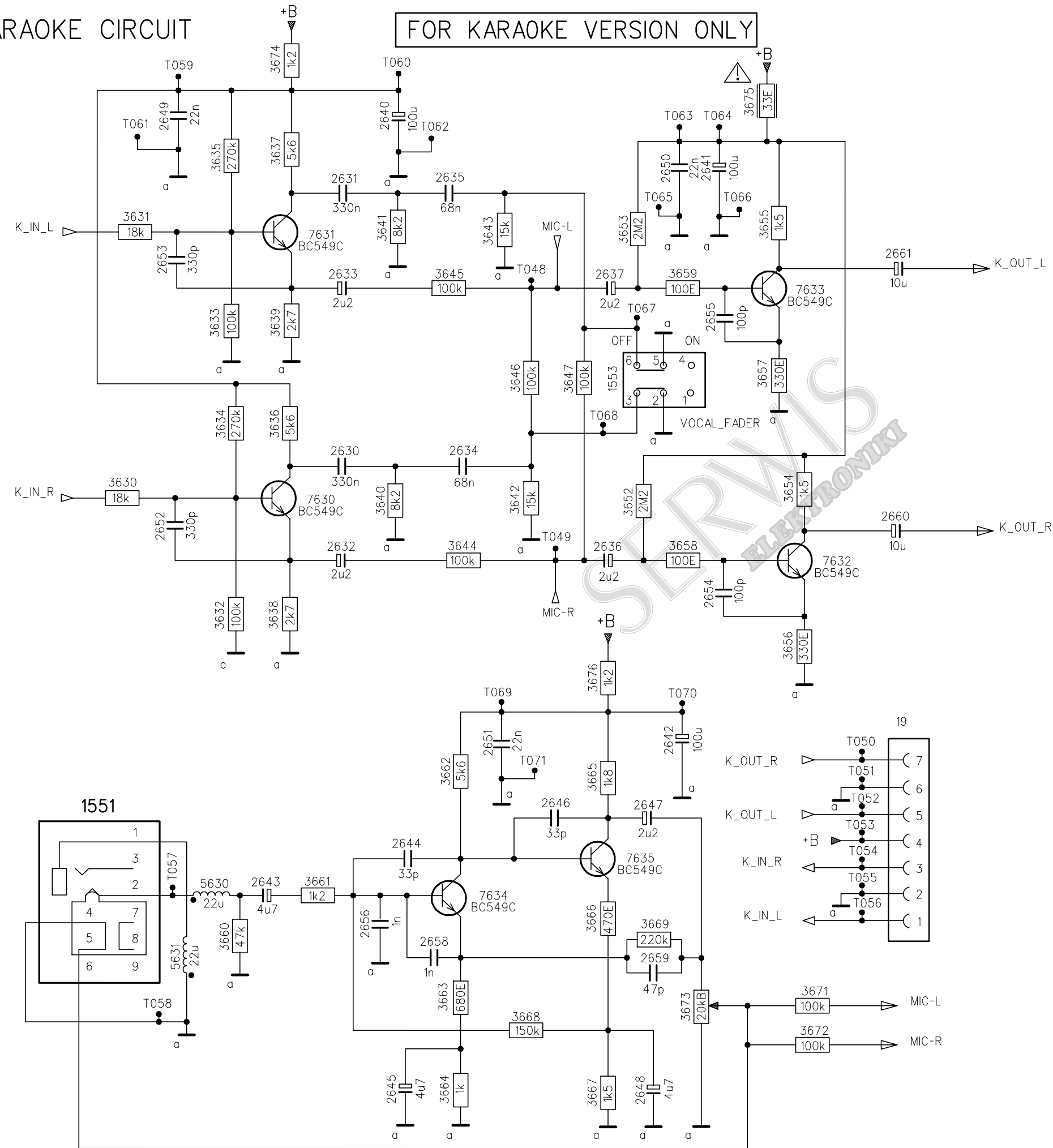


TRANSFORMER WINDING

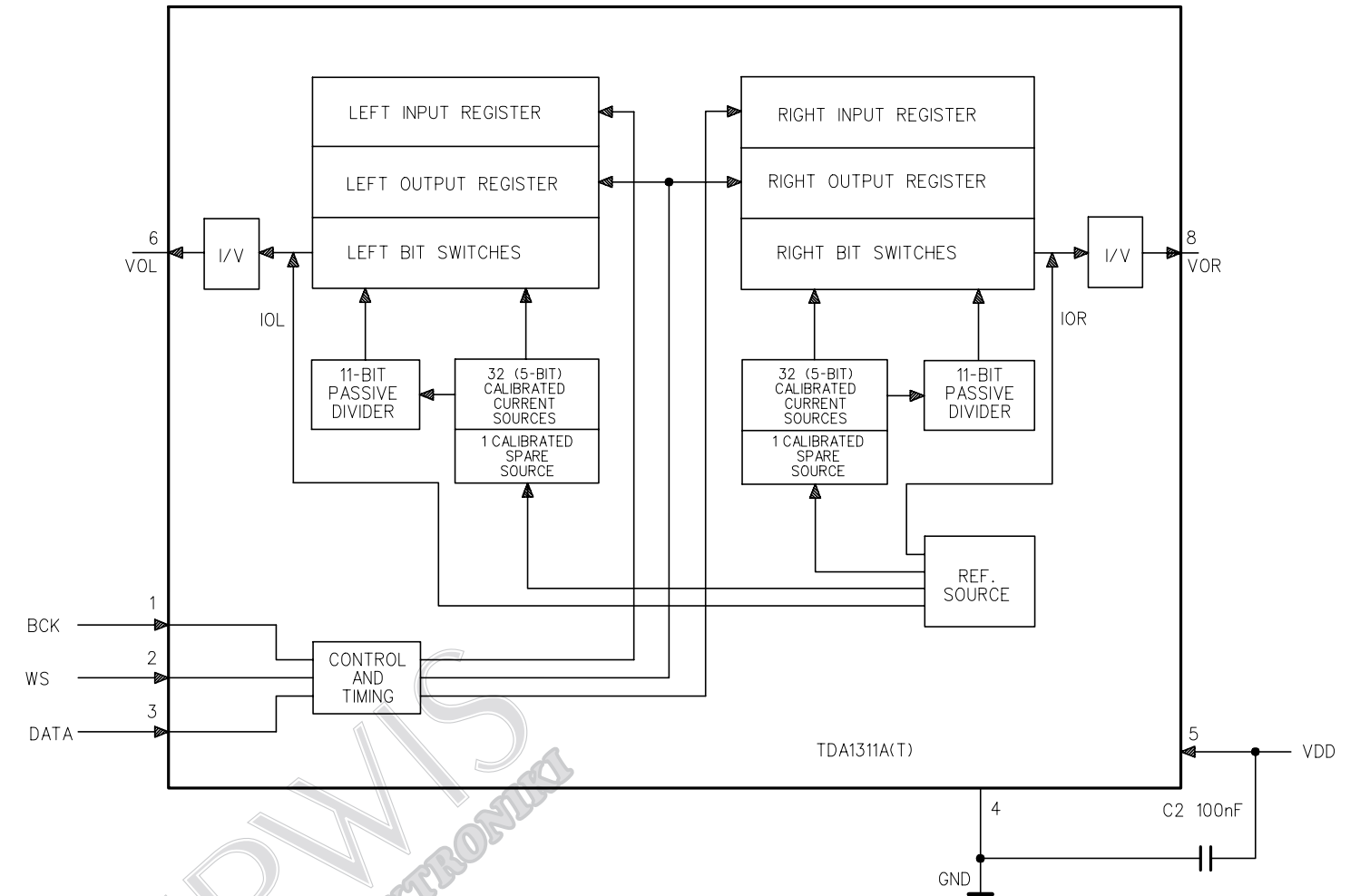


NOTE:
... V FOR POWER ON MODE ONLY
(... V) FOR REC MODE ONLY
* FOR CHROME FUNCTION (IEC II) VERSION
• NOT FOR CHROME FUNCTION VERSION
& SERVICE SOLUTION ONLY

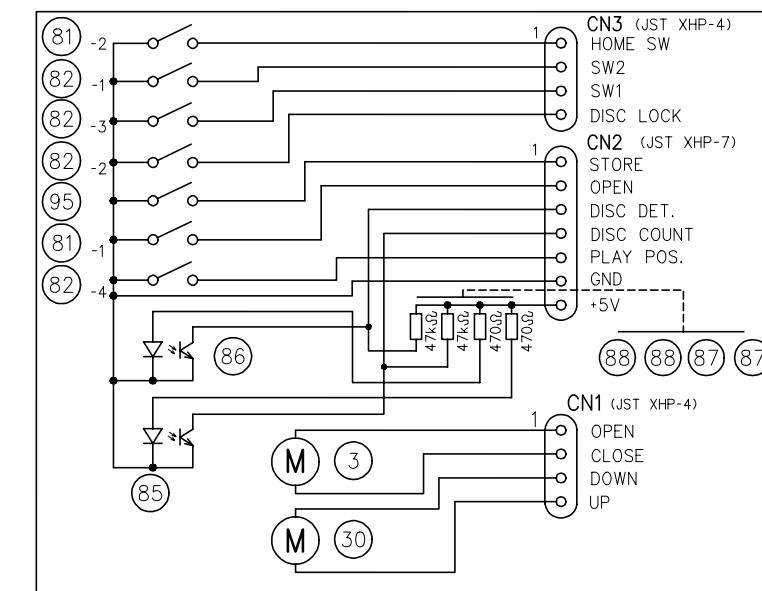
KARAOKE CIRCUIT



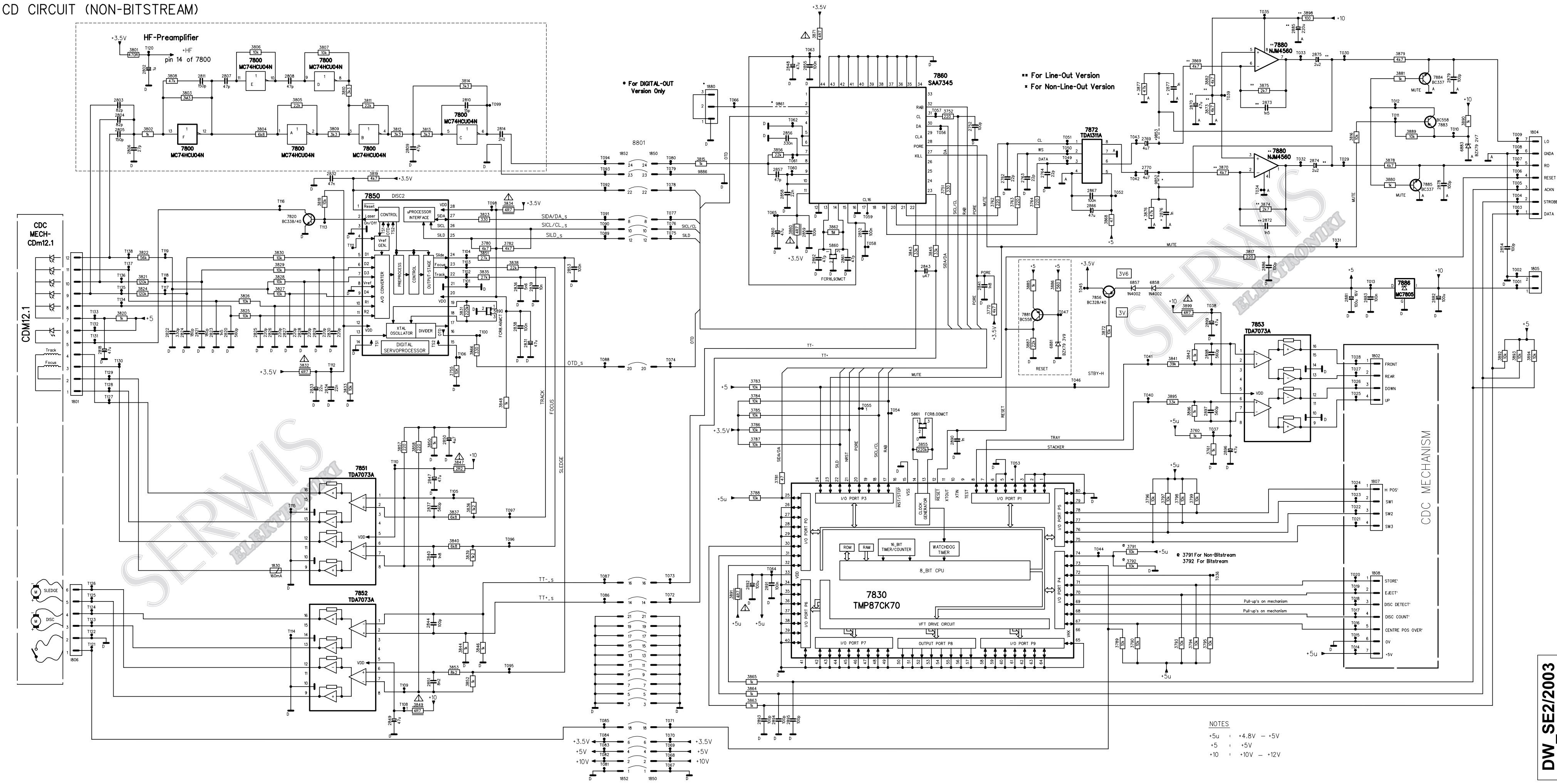
IC 7872 (TDA 1311A)



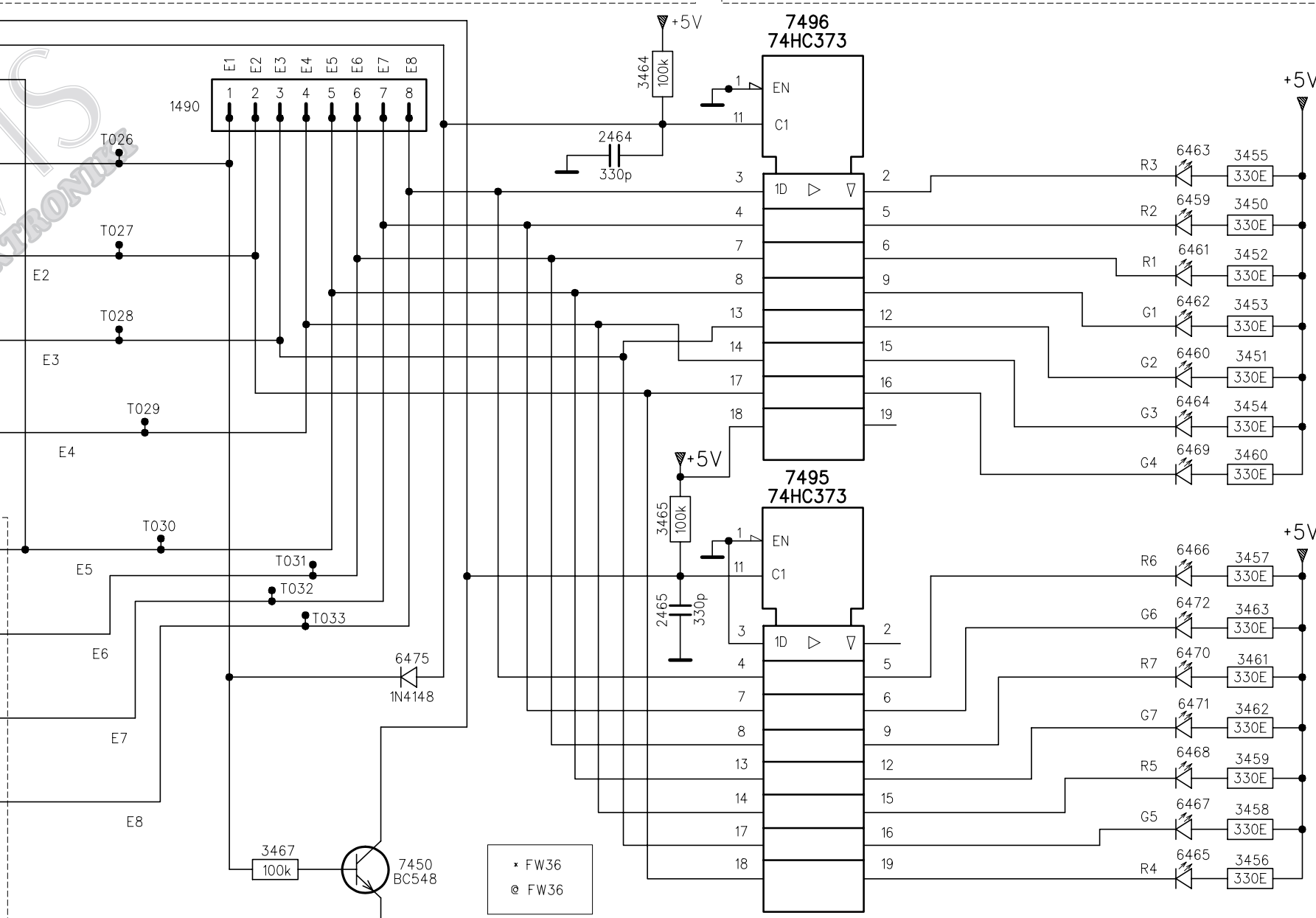
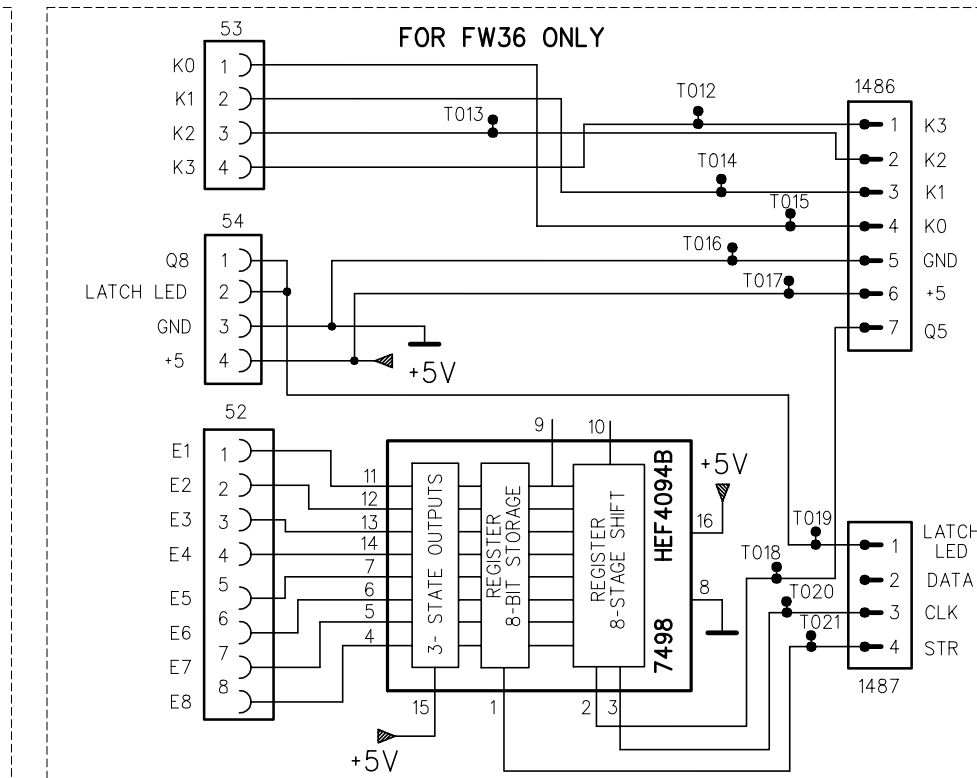
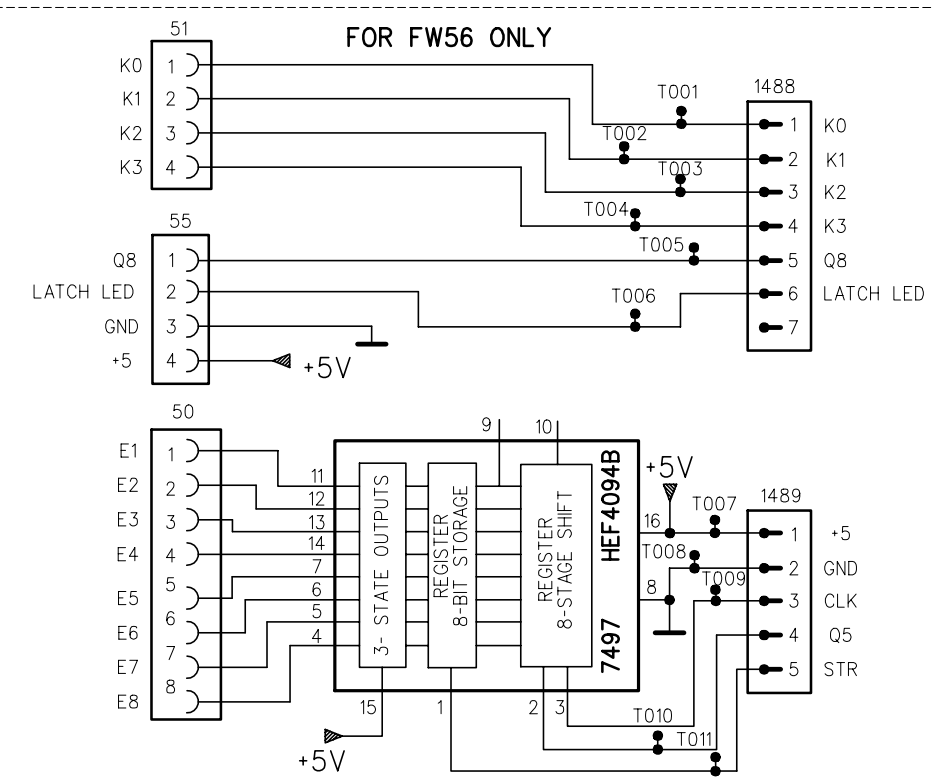
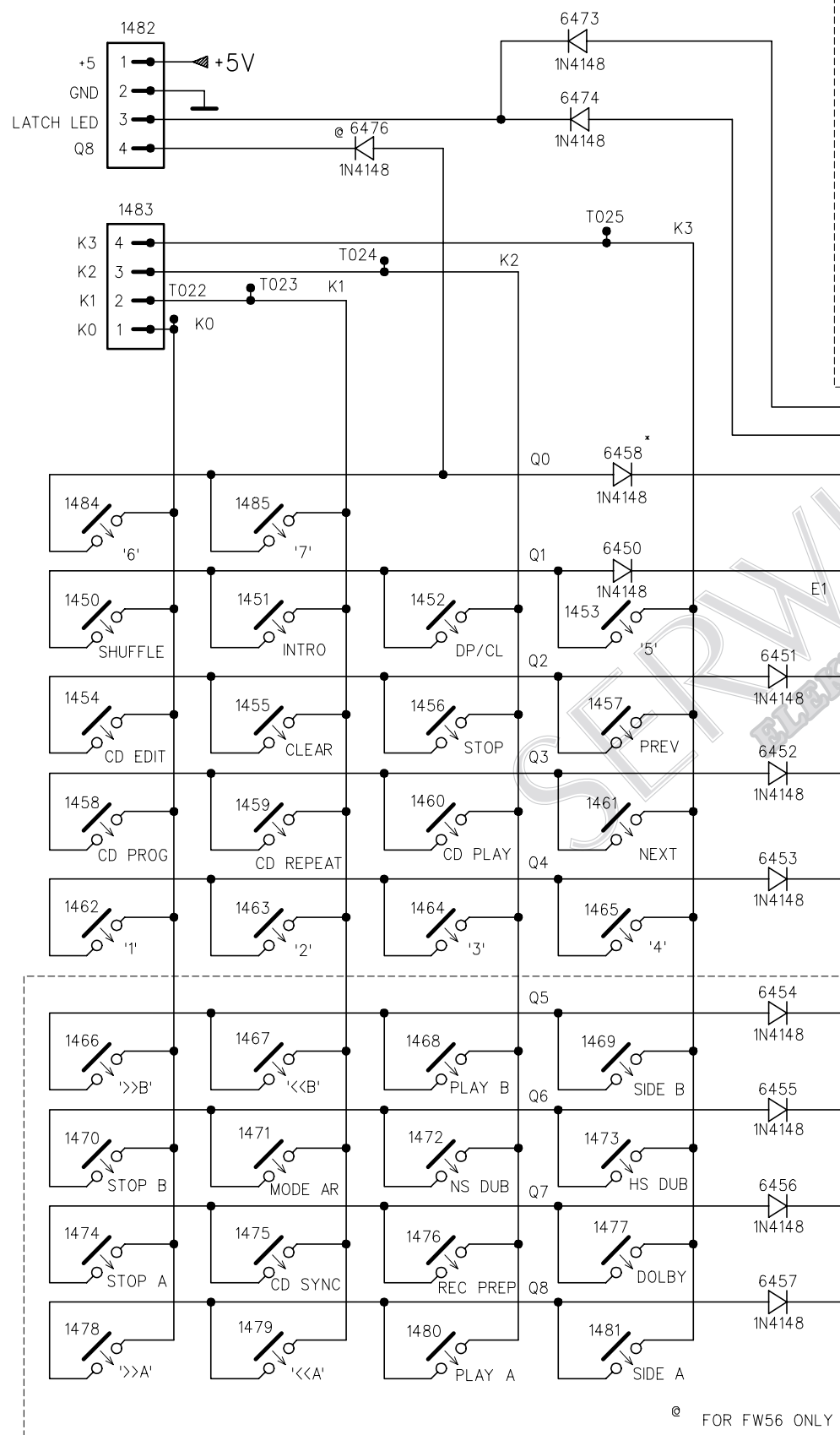
CDC MECHANISM ELECTRONICS



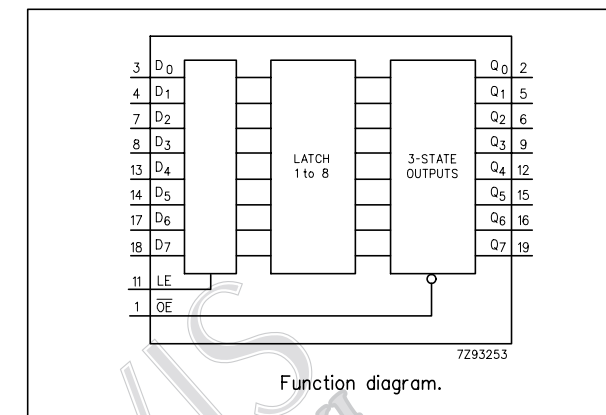
CD CIRCUIT (NON-BITSTREAM)



CDC KEY CIRCUIT



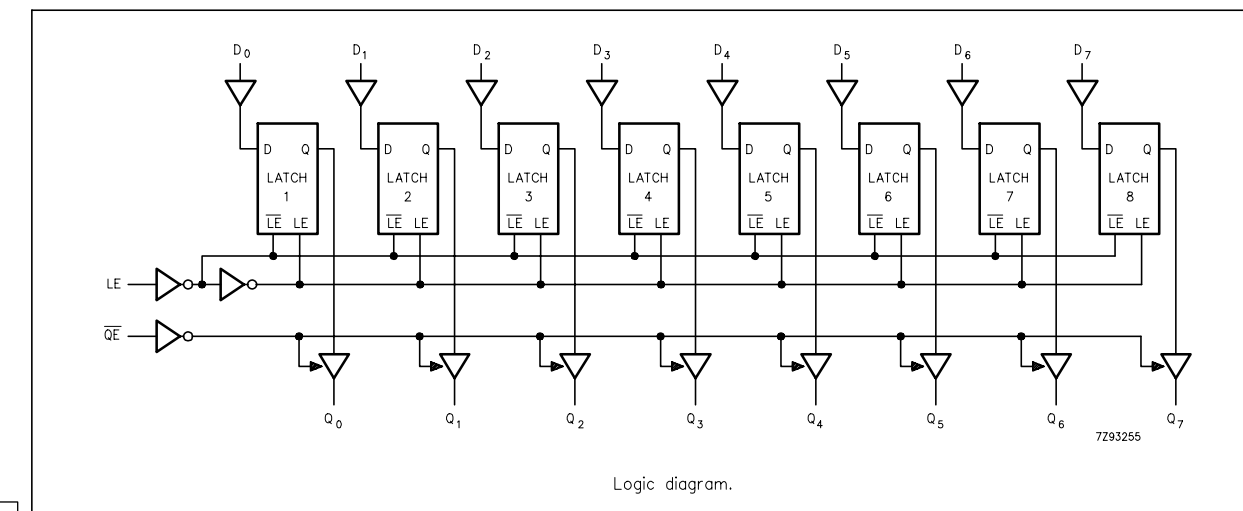
IC 7495 & 7496



FUNCTION TABLE

OPERATING MODES	INPUTS			INTERNAL LATCHES	OUTPUTS Q ₀ to Q ₇
	OE	LE	D _n		
enable and read register (transparent mode)	L	H	L	L	L
latch and read register	L	L	L	H	L
latch register disable outputs	H	L	L	H	Z

H = HIGH voltage level
h = HIGH voltage level one set-up time prior to the HIGH-to-LOW LE transition
L = LOW voltage level
l = LOW voltage level one set-up time prior to the HIGH-to-LOW LE transition
Z = high impedance OFF-state



DW_SE2/2003

Zestaw audio Philips FW36/20/21/22/25/30/37
(strona 9/12)

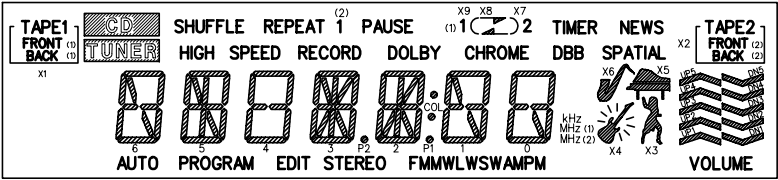


Figure 7. LCD pins connections

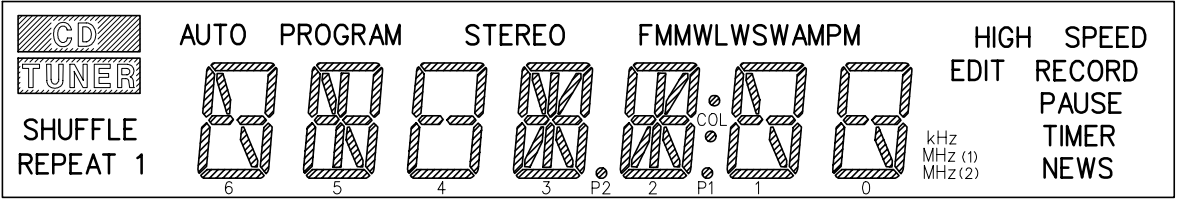
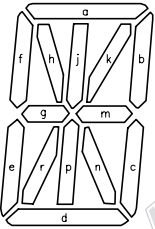
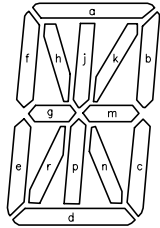


Figure 8. LCD pins connections



FW36/37 LCD DISPLAY PIN CONNECTIONS

PIN NO.	COM 0	COM 1	COM 2	COM 3
1	-	-	COM 2	-
2	-	-	-	COM 3
3	6gm	6f	6e	6n
4	6b	6a	6c	6d
5	FRONT (1)	6h	BACK (1)	TAPE1, X1
6	SHUFFLE	CD	HIGH SPEED	AUTO
7	5g	5f	5e	PROGRAM
8	5b	5a	5c	5d
9	TUNER	5hn	5m	5jp
10	1 (1), 2	TIMER	RECORD	REPEAT
11	4gm	4f	4e	EDIT
12	4b	4a	4c	4d
13	X7	X9	X8	1 (2)
14	FRONT (2)	X6	BACK (2)	TAPE 2, X2
15	3g	3f	3e	3n
16	3b	3a	3c	3d
17	3k	3h	3m	3jp
18	3r	DBB	PAUSE	STEREO
19	2gm	2f	2e	2n
20	2b	2a	2c	2d
21	2kr	CHROME	DOLBY	2jp
22	LW	-	MW	-
23	1gm	1f	1e	1n
24	1b	1a	1c	1d
25	COL	1h	FM, P1, MHz (1)	SW, P2, MHz (2)
26	X4	kHz	PM	AM
27	0gm	Of	0e	0n
28	0b	0a	0c	0d
29	X3	X5	SPATIAL	NEWS
30	UP1	DN1	-	VOLUME
31	-	-	-	-
32	-	-	-	-
33	DN2	DN3	DN4	DN5
34	UP2	UP3	UP4	UP5
35	COM 0	-	-	-
36	-	COM 1	-	-

FW36/37 LCD DISPLAY PIN CONNECTIONS

PIN NO.	COM 0	COM 1	COM 2	COM 3
1	-	-	COM 2	-
2	-	-	-	COM 3
3	6gm	6f	6e	6n
4	6b	6a	6c	6d
5	-	6h	-	-
6	SHUFFLE	CD	HIGH SPEED	AUTO
7	5g	5f	5e	PROGRAM
8	5b	5a	5c	5d
9	TUNER	5hn	5m	5jp
10	-	TIMER	RECORD	REPEAT
11	4gm	4f	4e	EDIT
12	4b	4a	4c	4d
13	-	-	-	1
14	-	-	-	-
15	3g	3f	3e	3n
16	3b	3a	3c	3d
17	3k	3h	3m	3jp
18	3r	-	PAUSE	STEREO
19	2gm	2f	2e	2n
20	2b	2a	2c	2d
21	2kr	-	-	2jp
22	LW	-	MW	-
23	1gm	1f	1e	1n
24	1b	1a	1c	1d
25	COL	1h	FM, P1, MHz (1)	SW, P2, MHz (2)
26	-	kHz	PM	AM
27	0gm	Of	0e	0n
28	0b	0a	0c	0d
29	-	-	-	NEWS
30	-	-	-	-
31	COM 0	-	-	-
32	-	COM 1	-	-

FRONT CIRCUIT II (GE PART)

